



Attorney Docket: 068354.1441

EV449865873US

8-604  
JW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:

Catherwood et al.

Serial No.: 09/870,463

Filing Date: June 1, 2001

Title: "Shadow Register Array Control  
Instructions"

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Group Art Unit: 2183

Examiner: Colman, Eric

Attny. Docket No. 068354.1441

Client Ref.: MTI-1669.US.0

MAIL STOP AMENDMENTS

Commissioner for Patents

P.O. Box 1450

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*Paul N. Katz*

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DATE OF MAILING: JULY 14, 2004

INFORMATION DISCLOSURE STATEMENT

Sir:

Applicants respectfully request, pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified application. Copies of the cited references in PDF formats on a CD are enclosed for the convenience of the Examiner.

Furthermore, pursuant to 37 C.F.R. §§1.97(g) and (h), no representation is made that these references are material to the patentability of the present application.

07/16/2004 MBLANCO 00000008 09870463  
01 FC:1806 180.00 OP

HOU02:995290.1

USSN 09/870,463  
Information Disclosure Statement

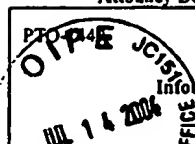
As the Information Disclosure Statement is being submitted along with a Response to Office Action, Applicants enclose a check in the amount of \$180.00. Applicants believe that no further fee is required, however, if an additional fee is required, please accept this transmittal as a petition therefor and charge any fee to Baker Botts L.L.P. (*formerly, Baker & Botts, L.L.P.*) Deposit Account No. 50-2148, Order No. (068354.1441) for any other charges necessary for the filing of this Information Disclosure Statement.

BAKER BOTTS L.L.P. (31625)

Date: July 14, 2004

By: 

Paul N. Katz, Reg. No. 35,917  
One Shell Plaza  
910 Louisiana Street  
Houston, Texas 77002-4995  
Telephone: 713.229.1343  
Facsimile: 713.229.7743  
AGENT FOR APPLICANTS

	Application No. <b>09/870,463</b>	Applicant(s): <b>CATHERWOOD et al.</b>	
	Docket Number <b>068354.1441</b>	Group Art Unit <b>2183</b>	Filing Date <b>June 1, 2001</b>

## U.S. PATENT DOCUMENTS

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A.	4779191	10/88	Greenblatt, Richard	711	2	
B.	3886524	5/75	Appelt, Daren R.	710	110	
C.	3771146	11/73	Cotton et al.	710	260	
D.	4074353	2/78	Woods et al.	710	264	
E.	6412081	6/02	Koscal et al.	714	34	
F.	5638524	6/10/97	Kiuchi et al.			
G.	5936870	8/99	Im, Jin Hyeock	708	552	
H.	6377619	4/02	Denk et al.	708	322	
I.	5517436	5/96	Andreas et al.	708	524	
J.	6564238	5/03	Kim et al.	708	513	

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
K.	0 855 643 A1	29-07-98	EP				
L.	0 554 917 A2	11-08-93	EP				

## NON-PATENT DOCUMENTS

	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
M.	Moon B I et al.: "A 32-bit RISC Microprocessor with DSP Functionality: Rapid Prototyping" IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, Institute of Electronics Information and Comm. Eng. Tokyo, JP, vol. E84-A no. 5, pages 1339-1347, XP001060025 ISSN: 0916-8508.	May 2001
N.	Turley J: "Balancing Conflicting Requirements When Mixing RISC, DSPs" Computer Design, Pennwell Publ. Littleton, Massachusetts, IS, vol. 37, no. 10, XP000860706 ISSN:0010-4566, printed from <www.austinlibrary.com:2081/DeliveryPrintSave.asp?tb=1& uh=btn+Y+md+B+EE>, 9 pages.	October 1998
O.	Levy M: "Microprocessor and DSP Technologies Unite for Embedded Applications" EDN Electrical Design News, Cahners Publishing Co., Newtown Massachusetts, US, no. Europe, XP000779113 ISSN: 0012-7515, printed from <www.reed-electronics.com/ednmag/archives/1998/030298/05df_01.htm>, 9 pages.	2 March 1998

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

PTO-1449  Information Disclosure Citation in an Application		Application No. <b>09/870,463</b>		Applicant(s): <b>CATHERWOOD et al.</b>			
		Docket Number <b>068354.1441</b>		Group Art Unit <b>2183</b>		Filing Date <b>June 1, 2001</b>	

U.S. PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	P.	5765216	6/98	Weng et al.	711	214	
	Q.	4451885	5/84	Gerson et al.	708	200	
	R.	6061783	5/00	Harriman, Ward	712	224	
	S.	6523108	2/03	James et al.	712	224	
	T.	5682339	10/28/97	Tam			
	U.	5657484	8/12/97	Scarrá			
	V.	5651121	7/22/97	Davies			
	W.	4090250	5/78	Carlson et al.	712	234	
	X.	4025771	5/77	Lynch et al.	708	521	
	Y.	4408274	10/93	Wheatley et al	711	163	

FOREIGN PATENT DOCUMENTS								
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

NON-PATENT DOCUMENTS		
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<b>U.S. PATENT DOCUMENTS</b>							
		<b>DOCUMENT NO.</b>	<b>DATE</b>	<b>NAME</b>	<b>CLASS</b>	<b>SUBCLASS</b>	<b>FILING DATE</b>
	Z.	4379338	4/83	Nishitani et al.	708	552	
	AA	4841468	6/89	Miller et al.	708	625	
	BB	4926371	5/90	Vassiliadis et al.	708	628	
	CC	5121431	6/92	Wiener, Michael J.	713	174	
	DD	5422805	6/95	McIntyre et al.	708	625	
	EE	5740095	4/98	Parant, Phillippe	708	628	
	FF	6144980	11/00	Oberman, Stuart F.	708	627	
<b>FOREIGN PATENT DOCUMENTS</b>							
		<b>DOCUMENT NO.</b>	<b>DATE</b>	<b>COUNTRY</b>	<b>CLASS</b>	<b>SUBCLASS</b>	<b>TRANSLATION</b>
							YES    NO
<b>NON-PATENT DOCUMENTS</b>							
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